

Attorney's Docket No.:10559-391001/P10256 - ADI
APD1812-1-US

Moreover, Byrne describes an arrangement in which pipelined floating point execution units simultaneously receive identical instruction signals (see, inter alia, Byrne col. 6, lines 20-24). Therefore, with Byrne, instructions are initially fed to each pipeline as opposed to being piped from one pipeline to another.

Accordingly, the claims should be allowable.

Concluding Comments

It is believed that all of the pending claims have been addressed in this paper. However, failure to address a specific rejection, issue or comment, does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above are not intended to be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

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Applicant asks that all claims be allowed. Please apply
any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 1/5/06



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